

# NOVEL ACTIVE DIFFERENTIAL PHASE SPLITTERS IN RFIC FOR WIRELESS APPLICATIONS

Huainan Ma, Sher Jiun Fang, Fujiang Lin and Hiroshi Nakamura<sup>1</sup>

Institute of Microelectronics (IME), Singapore

11 Science Park Road, Science Park II, Singapore 117685, Singapore

<sup>1</sup>Oki Techno Centre (Singapore) Pte Ltd, Singapore 117674, Singapore

## ABSTRACT

Two novel active differential phase splitters have been designed and fabricated in a GaAs MESFET process. The new circuits employ a concept of feedback to adjust gain and phase unbalance separately and accurately. The active phase splitters feature simplicity, low power supply and wide-band performance. The circuits can provide  $\pm 1$  dB and  $180 \pm 1^\circ$  differential signals within 4 GHz bandwidth, well covering the frequency range currently used for dual mode commercial wireless communications. In narrow-band application more accurate balanced differential signals can be achieved by external tuning.

## INTRODUCTION

Differential phase splitters (or baluns) are basic cells required in microwave components such as balanced mixers and multipliers. An ideal differential phase splitter generates a pair of differential signals which have balanced amplitude and phase ( $0$  dB and  $180^\circ$ ) from a single input.

In RFIC there are passive and active baluns. LC networks can be used for narrow-band passive baluns, microstriplines can be used for wide-band passive baluns [1]. However, at lower microwave frequencies the passive L, C and microstriplines are too expensive due to their large physical size. There are three categories of MESFET active balun circuits normally employed at lower microwave frequencies for wireless applications: 1. Single FET circuits [2][3]; 2. Common-gate common-source circuits [4][5], and 3. Differential amplifier circuits [6][7].

A conventional MESFET differential pair is not a good candidate since the tail current source can only provide limited impedance due to the short channel effects and the strong parasitic effects at very high frequencies. Furthermore, low  $V_{DD}$  requirement for portable wireless applications ( $V_{DD} < 3V$  or even  $< 2V$ ) makes it impossible to have the tail current source at the bottom of the differential pair in RFIC, since it needs a substantial potential drop across itself ( $1 \sim 1.5$  V). In [6] the current source was replaced by an inductor to increase the high frequency impedance. However, at low microwave frequencies an inductor with a very big value is needed, which is very expensive and may have a very low resonant frequency. In many cases, a biasing resistor  $R_S$  is used

instead as shown in Fig. 1 (a). However, the output balance is poor due to its limited impedance, see Fig. 1 (b). To overcome this problem, a compensation method was invented [7], where complicated compensation circuits make the balun design more difficult and consume additional power.

This paper proposes two novel active differential phase splitters using a concept of feedback to adjust gain and phase unbalance separately and accurately. In the feedback

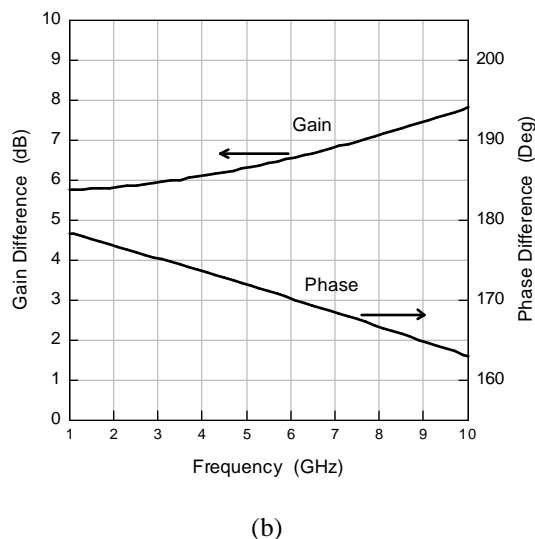
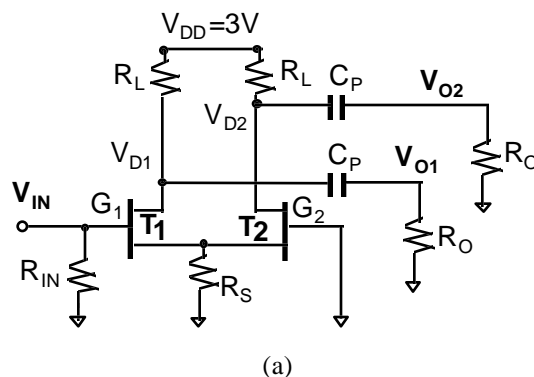


Fig. 1. (a) The simple differential pair with a biasing resistor  $R_S$ ; (b) Simulated gain and phase differences versus frequency for circuit (a).

circuit a tunable resistor can be realised by using a MESFET to tune the unbalance caused by the process variations. The circuits were realised successfully by using a 0.5- $\mu\text{m}$  MESFET technology with  $V_P = -0.9$  V. The simulation and measured results will be reported.

## CIRCUIT DESCRIPTION

**Circuit 1.** The first circuit is shown in Fig. 2. Compared with Fig. 1, a series LCR feedback circuit is proposed and connected from node D1 through G2 to ac ground. The feedback circuit consists of 2 resistors  $R_{G2}$  and  $R_F$ , an inductor  $L_F$  and a capacitor  $C_F$ . The resistor  $R_{G2}$  plays two roles: to keep dc bias of T2 the same as T1, and to sense the signal fed back from D1.  $C_F$  provides a dc block. The reactance of the feedback circuit between D1 and G2 is  $X_F = \omega L_F - 1 / (\omega C_F)$ . This means that  $X_F$  can be positive (inductive), zero (resistive) or negative (capacitive) by just adjusting  $L_F$  and  $C_F$  values at the application frequency  $\omega$ . Thus, the phase difference between the output port signals  $V_{O1}$  and  $V_{O2}$  can be effectively tuned. The amount of the signal fed back from node D1 to G2 can be adjusted by changing the ratio of  $R_F$  to  $R_{G2}$ . Thus adjusting the ratio of  $R_F$  to  $R_{G2}$  will effectively tune the amplitude unbalance between output port signals  $V_{O1}$  and  $V_{O2}$ . It is clear that the output termination impedance  $R_O$  will affect the choice of values of the components in the feedback circuit.

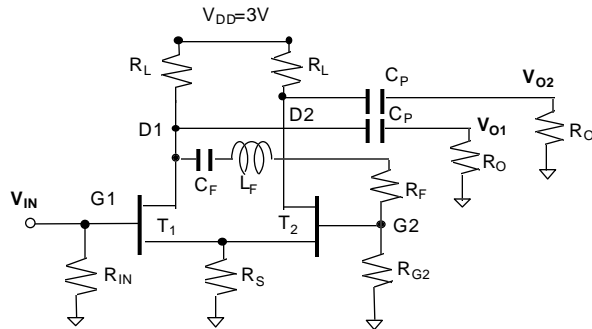


Fig. 2. Circuit 1, the differential phase splitter with a series LCR feedback balancing circuit.

**Circuit 2.** Fig. 3 shows the second circuit which is based on the first one but externally tunable. In this new circuit, one more MESFET T3 is connected in series with the feedback resistor  $R_F$ . The externally changeable dc voltage  $V_B$  will adjust the channel resistance of T3. Thus T3 and  $R_F$  are considered as a combined variable feedback resistor  $R_{TF}$ . T3 and  $R_F$  are relocated to node D1. This relocation keeps  $V_B$  operating in a positive range ( $0 < V_B < V_{DD}$ ) for the depletion MESFET which has the negative pinch-off voltage. In the circuit design, the initial bias point of  $V_B$ , the size of T3 and the values of  $R_F$  and  $R_{G2}$  are chosen in

such a way that external tuning of  $V_B$  will vary the gain and phase balance within a specific small range centered at the balanced point. In this way the unbalance (or off-set of gain) of the differential signals caused by the process variations can be effectively cancelled in high performance RF applications.

## SIMULATION AND MEASUREMENT

Both circuits were designed and optimized at 1.67 GHz

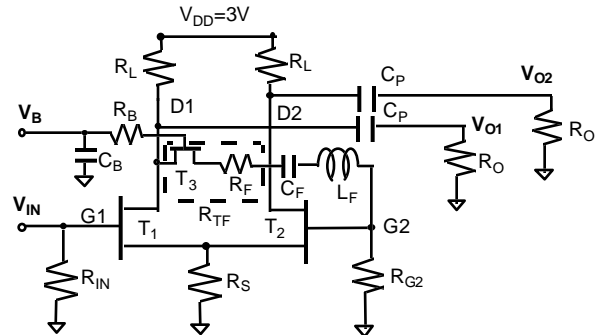


Fig. 3. Circuit 2, the tunable active differential phase splitter.

for the PHS application and the results were verified through on-wafer S-Parameter measurement.

Fig. 4 is the simulation and measurement results of Circuit 1. The gain difference of  $\pm 1$  dB and phase difference of  $180 \pm 1^\circ$  have been easily achieved within 0.5 ~ 3.5 GHz frequency range, which covers all frequencies currently used for commercial wireless communication systems. If the circuit was optimized for a wider band application, the

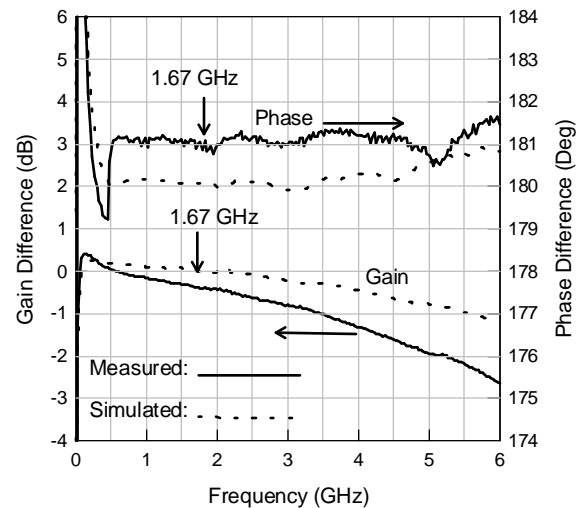


Fig. 4. Simulated and measured gain and phase differences versus frequency for Circuit 1.

same results could be obtained within 4 GHz bandwidth. Since the circuit is based on the symmetrical differential pair and has a broadband performance, this feature makes it have the best performance against IC process variations. Table 1 is the simulation of gain and phase difference against variation or mismatch of parameters in the circuit. From Table 1, it is seen that the circuit has an excellent process tolerance. Fig. 5 is the measured performance of Circuit 1 against variation of  $V_{DD}$ .

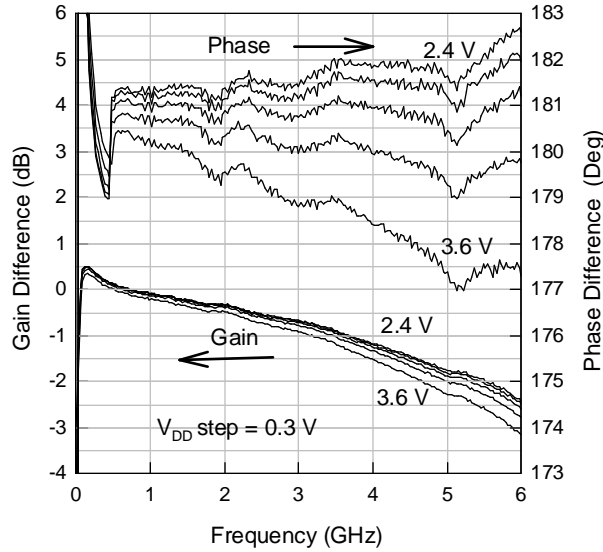


Fig. 5. Measured gain and phase differences versus frequency with variation of  $V_{DD}$  for Circuit 1.

For Circuit 2, Fig. 6 shows measured gain and phase differences versus frequency when tuning  $V_B$  externally. At 1.67 GHz (optimized operating frequency), by tuning external dc voltage  $V_B$ , the gain difference can vary from  $-3$  to  $+2$  dB while the change of phase difference is within  $3^\circ$ . With Circuit 2, the RF mismatch or off-set (not dc off-set) of the differential amplifier next to Circuit 2 can be cancelled by this external tuning. Therefore, Circuit 2 provides a critical unbalance cancellation technique that can be used in high performance RFICs.

Both circuits consume 3.8 mA dc current. Their chip photos are shown in Fig. 7. Obviously, the feedback circuits do not consume any extra dc current. They are easy to understand and design.

## CONCLUSIONS

Two novel active differential phase splitters with LCR series feedback circuits have been designed, measured and discussed. The feedback circuits can adjust gain and phase unbalance separately and accurately, without consuming

extra dc current. The new circuits have excellent performance against process variations due to their inherent symmetrical topology. The circuits can achieve  $\pm 1$  dB and  $180 \pm 1^\circ$  differential signals within 4 GHz bandwidth, well covering the frequencies for dual mode wireless communication. With the tunable feedback circuit more accurately balanced differential signals can be achieved by external tuning. This circuit topology can be extended to other technologies like CMOS, BiCMOS or Bipolar for RF applications. Compared with other active baluns, the new circuits feature simplicity, low power supply, wide-band performance, and consume small die

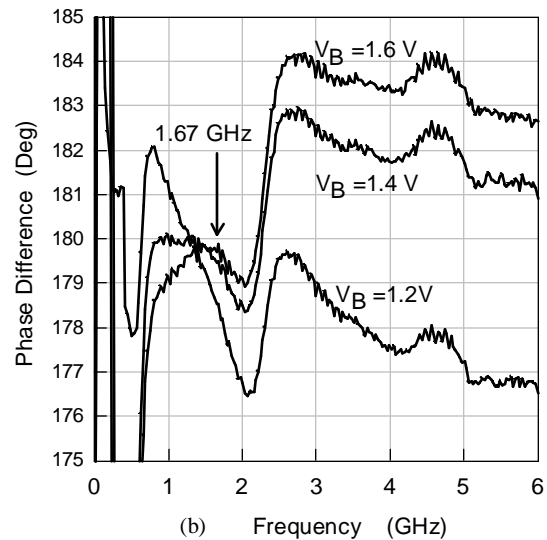
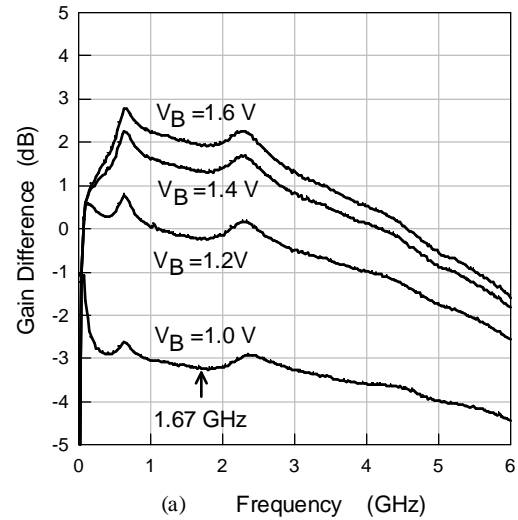


Fig. 6. Measured gain difference (a) and phase difference (b) versus frequency with external tuning of  $V_B$ .

area at lower microwave frequencies.

## ACKNOWLEDGMENT

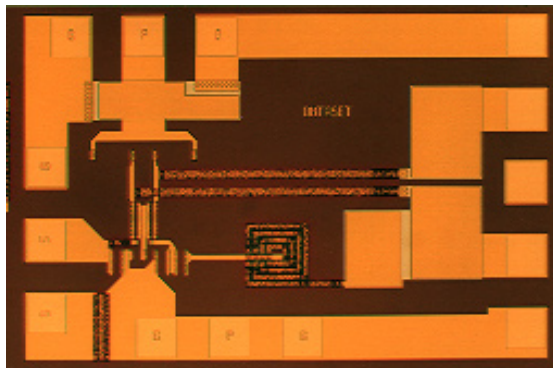
The authors would like to thank Components Division of Oki Electric Industry Co., Ltd for their circuit fabrication.

## REFERENCES

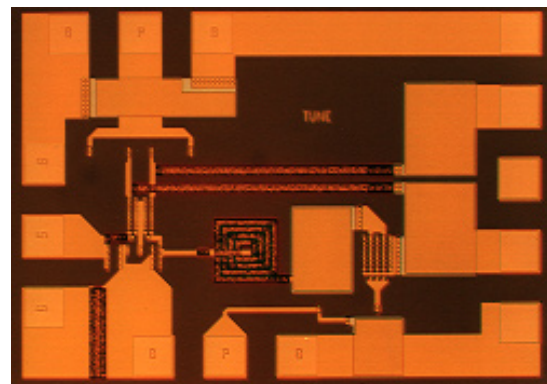
1. M. C. Tsai, "A New Compact Wideband Balun," *1993 IEEE MTT-S Digest*, pp. 141-143.
2. M. E. Goldfarb et al "A Novel MMIC Biphas Modulator with Variable Gain Using Enhancement-Mode FETS Suitable for 3 V Wireless Applications", *Proc. 1994 IEEE Microwave and Millimeter-Wave Monolithic Circuits Symposium*, pp. 99-102.
3. H. Koizumi et al, "A GaAs Single Balanced Mixer MMIC with Built-in Active Balun for Personal Communication Systems", *Proc. 1995 IEEE Microwave and Millimeter-Wave Monolithic Circuits Symposium*, pp. 77-80.
4. L. M. Devlin et al, "A 2.4GHz Single Chip Transceiver", *Proc. 1993 IEEE Microwave and Millimeter-Wave Monolithic Circuits Symposium*. pp. 23-26.
5. H. Ma, et al, "A High Performance GaAs MMIC Upconverter with an Automatic Gain Control Amplifier, " *1997 IEEE GaAs IC Symposium, Technical Digest*, pp. 232-235
6. Y. Xuan et al, "Computer-Aided Design of Microwave Frequency Doublers Using a New Circuit Structure," *IEEE Trans. Microwave Theory Tech.*, vol. 41, pp. 2264 -2268, Dec. 1993.
7. W. H. Hayward, et al , "Compensation Method and Apparatus for Enhancing Single Ended to Differential Conversion," US patent, 5,068,621

TABLE 1.  
Simulated Gain and Phase Difference Against Process Variations

Circuit 1	Mismatch Values	Optimized@1.67GHz		Optimized@5.8GHz	
		$\Delta$ Gain (dB)	$\Delta$ Phase (Deg)	$\Delta$ Gain (dB)	$\Delta$ Phase (Deg)
$\Delta V_P$	$\pm 20\text{mV}$	$-0.15 \sim 0.11$	$179.5 \sim 179.6$	$-0.16 \sim 0.16$	$180.0 \sim 180.1$
$\Delta C_F$	$\pm 20\%$	$-0.026 \sim -0.027$	$179.7 \sim 179.4$	$-0.0045 \sim -0.0037$	$180.2 \sim 179.9$
$\Delta C_P$	$\pm 10\%$	$-0.027 \sim -0.026$	$179.7 \sim 179.4$	$0.0011 \sim -0.032$	$178.9 \sim 180.2$
$\Delta R_L$	$\pm 10\%$	$0.17 \sim -0.26$	$179.2 \sim 179.9$	$0.21 \sim -0.16$	$179.2 \sim 180.9$
$\Delta (R_F / R_{G2})$	$\pm 10\%$	$0.17 \sim -0.26$	$179.2 \sim 179.9$	$0.34 \sim -0.39$	$179.5 \sim 180.6$



(a)



(b)

Fig. 7. Chip microphotos of Circuit 1 (a) and Circuit 2 (b).